

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L8	168	((multilayer multi adj (layer)) with ceramic with substrate) same electrode and (resin)	USPAT	2009/02/16 19:10
2	BRS	L9	229	((multilayer multi adj (layer)) with ceramic with substrate) same electrode and (resin)	US-PGPUB; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2009/02/16 19:20
3	BRS	L12	1575	((multilayer multi adj layer) with ceramic with substrate) and (device element component) and (resin insulating insulation dielectric) and (contact pad plug bump ball electrode) and (power ground reference adj voltage)	USPAT	2009/02/16 19:29
4	BRS	L13	235	12 and overlap\$5	USPAT	2009/02/16 19:29
5	BRS	L14	1025	((multilayer multi adj layer) with ceramic with substrate) and (device element component) and (resin insulating insulation dielectric) and (contact pad plug bump ball electrode) and (power ground reference adj voltage)	US-PGPUB; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2009/02/16 19:38
6	BRS	L15	182	14 and overlap\$5	US-PGPUB; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2009/02/16 19:39
7	BRS	L16	18	((multilayer multi adj layer) with ceramic with substrate) and (device element component) and (resin insulating insulation dielectric) and (contact pad plug bump ball electrode) and (power ground reference adj voltage)).clm.	US-PGPUB	2009/02/16 20:47
8	BRS	L17	1	((multilayer multi adj layer) with ceramic with substrate) and (device element component) and (resin insulating insulation dielectric) and (contact pad plug bump ball electrode) and (power ground reference adj voltage) and (overlap\$5).clm.	US-PGPUB	2009/02/16 20:48